



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2811
Examiner: Quang D. Vu

In re PATENT APPLICATION of

Applicant : Kazutaka SHIBATA

Appln. No. : 10/608,016

Filed : June 30, 2003

For : SEMICONDUCTOR DEVICE AND
MANUFACTURING METHOD THEREOF

Atty. Dkt. : AI 288

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In further response to the Final Examiner's Action mailed February 27, 2006, please
amend the above-identified application as follows:

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)
) **AMENDMENT AFTER**
) **FINAL ACTION**
)
)
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Jmr 6/10/2006

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2811

Examiner: Junghwa M. IM

In Re PATENT APPLICATION Of:

Applicant: Kazutaka SHIBATA

Serial No.: 10/608,016

Filed: June 30, 2003

For: SEMICONDUCTOR DEVICE AND
MANUFACTURING METHOD THEREOF

Docket No.: AI 288

AMENDMENT
AFTER
FINAL ACTION

October 22, 2007

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

OK to enter

Jm2

10/31/2007

Sir:

This amendment is filed in response to the Examiner's Final Action mailed on July 25, 2007. Please amend the above-identified application as follows: